

# United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, D.C. 20231
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/766,896	01/22/2001	Hidemitsu Aoki	WAM-03401	2616
26339 75	590 02/14/2003			
PATENT GROUP CHOATE, HALL & STEWART EXCHANGE PLACE, 53 STATE STREET			EXAMINER	
			DEO, DUY VU NGUYEN	
BOSTON, MA	02109		ART UNIT	PAPER NUMBER
			1765	1.1
			DATE MAILED: 02/14/2003	14

Please find below and/or attached an Office communication concerning this application or proceeding.

			4
	Application No.	Applicant(s)	
	09/766,896	AOKI ET AL.	
Office Action Summary	Examiner	Art Unit	
	DuyVu n Deo	1765	
The MAILING DATE of this communication app Period for Reply	pears on the cover s	neet with the correspondence addr ss	<b></b> ,
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a repl - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).  Status	36(a). In no event, howevery within the statutory minim will apply and will expire SIX	er, may a reply be timely filed  um of thirty (30) days will be considered timely.  ( (6) MONTHS from the mailing date of this communic ecome ABANDONED (35 U.S.C. § 133).	ation.
1) Responsive to communication(s) filed on <u>02</u> .	January 2003 .		
24/2	nis action is non-fina		
3) Since this application is in condition for allow closed in accordance with the practice under Disposition of Claims	ance except for for Ex parte Quayle, 1	nal matters, prosecution as to the mer 935 C.D. 11, 453 O.G. 213.	rits is
4) Claim(s) <u>10-14,16-20,22-26,28 and 29</u> is/are	pending in the appl	ication.	
4a) Of the above claim(s) is/are withdra			
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>10-14,16-20, 22-26, 28, 29</u> is/are reje	ected.		
7) Claim(s) is/are objected to			
8) Claim(s) are subject to restriction and/o	or election requirem	ent.	
Application Papers			
9) The specification is objected to by the Examine		Lie butha Evaminar	
10) The drawing(s) filed on is/are: a) acce			
Applicant may not request that any objection to the state of the state	ne drawing(s) be neid	In abeyance. See 37 CTN 1.05(a).	
If approved, corrected drawings are required in re			
12) The oath or declaration is objected to by the E			
Priority under 35 U.S.C. §§ 119 and 120			
13) Acknowledgment is made of a claim for foreig	in priority under 35	U.S.C. § 119(a)-(d) or (f).	
a) ☐ All b) ☐ Some * c) ☐ None of:	, ,	•	
1. ☐ Certified copies of the priority documen	its have been recei	ved.	
2. Certified copies of the priority documen			
Copies of the certified copies of the price application from the International B     See the attached detailed Office action for a lis	ority documents havureau (PCT Rule 1 t of the certified cop	ve been received in this National Stag 7.2(a)). bies not received.	
14) Acknowledgment is made of a claim for domes	tic priority under 35	U.S.C. § 119(e) (to a provisional app	lication).
a) The translation of the foreign language portion 15) Acknowledgment is made of a claim for domes	rovisional applicatio	n has been received.	
Attachment(s)			
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) 🔲	Interview Summary (PTO-413) Paper No(s) Notice of Informal Patent Application (PTO-152 Other:	)

Application/Control Number: 09/766,896

Art Unit: 1765

#### **DETAILED ACTION**

## Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 10-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Small (US 5,981,454) and Tanabe et al. (US 6,068,000).

Small describes a method for removal of chemical residues, which have been applied in a previous step, from the metal surface or dielectric surface of a semiconductor substrate using a solution comprising: gallic acid (hydroxyl aromatic compound), urea hydroperoxide (urea or an urea derivative), water, and hydroxylamine (col. 7, line 10, 35; col. 8, line 27; table III; claim 1). Unlike claimed invention, Small doesn't describe a water-soluble organic solvent Tanabe describes a solution for cleaning or removing degenerate resist on a metal where he teaches using water-soluble organic solvent (summery, col. 5, line 45-59). It would have been obvious for one skilled in the art to add a water-soluble organic solvent in light of Tanabe because Tanabe teaches that it would exhibit a high anticorrosive effect for metallic films.

The concentration of gallic acid, urea hydroperoxide, hydroxylamine, and water are 1-25 wt%, 0.5-30 wt%, 30 wt%, 5 wt% respectively (col. 7, line 22; col. 8, line 29; table III).



Art Unit: 1765

3. Claims 16-20, 22-26, 28, 29 are rejected under 35 U.S.C. 103(a) as being unpatentable over Small, Tanabe and Zhao et al. (US 6,204,192).

Unlike claimed invention, Small is silent about prior steps of patterning the dielectric layer to expose the metal layer including steps of forming a metal film (copper), 1<sup>st</sup> dielectric film, and resist or 2<sup>nd</sup> dielectric film, etching the 1<sup>st</sup> dielectric layer using the resist film or 2<sup>nd</sup> dielectric layer as a mask to expose the metal layer. However, these steps are well known to one skill in the art as shown here by Zhao (fig. 2; col. 4). It would have been obvious to one skill in the art at the time of the invention that Small's method can be used in any semiconductor process, such as a single or dual damascene process taught by Zhao, that has dielectric and metal layer in order to clean the dielectric and metal layers with an expected result.

4. Claims 10-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Morinaga et al. (US 5,885,362) and Tanabe et al. (US 6,068,000).

Morinaga describes a method for cleaning semiconductor, metal, and glass surfaces using a solution comprising pyrogallol (hydroxy aromatic compound), urea and its derivatives, dimethylenethanolamine (alkanolamine), and water (col. 3, line 37-42; col. 5, line 10-12; col. 7, line 22; col. 8, line 34-40). Even though Morinaga doesn't describes stripping of resist or etching residues from the semiconductor substrate having exposed metal film. However, he teaches using the solution for cleaning/etching of semiconductor, metal, and glass surface (col. 13, line 55-63); therefore, it would have been obvious at the time of the invention for one skill in the art to use the solution in cleaning etching residues from the semiconductor wafer having exposed metal film in order to obtain a clean wafer.



Art Unit: 1765

Unlike claimed invention, Morinaga doesn't describe a water-soluble organic solvent Tanabe describes a solution for cleaning or removing degenerate resist on a metal where he teaches using water soluble organic solvent (summery, col. 5, line 45-59). It would have been obvious for one skilled in the art to add a water soluble organic solvent in light of Tanabe because Tanabe teaches that it would exhibit a high anticorrosive effect for metallic films.

Referring to claim 11, as shown in Morinaga, the amount of each of compound in the solution is determined through test runs in order to see the effective of various concentrations. Therefore, they are result-effective variables and would have been obvious for one skilled in the art to determine the compound concentrations through routine experimentation to achieve optimum concentration in order to clean the wafer with a reasonable expectation of success. See *In re Boesch*, 617 F .2d 272, 205 USPQ 215 (CCPA 1980).

5. Claims 16-20, 22-26, 28, 29 are rejected under 35 U.S.C. 103(a) as being unpatentable over Morinaga, Tanabe, and Zhao et al. (US 6,204,192).

Unlike claimed invention, Morinaga is silent about prior steps forming a semiconductor wafer including steps of forming a metal film (copper), 1<sup>st</sup> dielectric film, and resist or 2<sup>nd</sup> dielectric film; etching the 1<sup>st</sup> dielectric layer using the resist film or 2<sup>nd</sup> dielectric layer as a mask to expose the metal layer. However, these steps are well known to one skill in the art as shown here by Zhao (fig. 2; col. 4). It would have been obvious to one skill in the art at the time of the invention that Morinaga's method can be used in any semiconductor process, such as a single or dual damascene process taught by Zhao, because Morinaga teaches that his solution can be used to clean/etch semiconductor substrate and prevents a substrate from being deposited with metal impurities (col. 3, line 13-18).

Application/Control Number: 09/766,896

Art Unit: 1765

## Response to Arguments

6. Applicant's arguments filed 1/2/03 have been fully considered but they are not persuasive.

Referring to applicant's argument that Tanabe doesn't describe using water-soluble organic solvent to strip a resist film. Please see col. 5, line 40-55 where he teaches the water-soluble organic solvent is used in lithographic rinsing solution, which would have to remove resist after ashing (fig. 2). This is the same used as claimed solution as described in page 18-19. furthermore, he describes that the water-soluble organic solvent, when used for removing resist pattern, in the remover solution can exhibit a high anticorrosive effect for metallic-thin films (col. 5, line 50-55).

Applicant's argument that Small, Morinaga, and Zhao do no describe using the water-soluble organic solvent is acknowledged. In response to applicant's arguments against the references individually, one cannot show nonobviousness by attacking references individually where the rejections are based on combinations of references. See *In re Keller*, 642 F.2d 413, 208 USPQ 871 (CCPA 1981); *In re Merck & Co.*, 800 F.2d 1091, 231 USPQ 375 (Fed. Cir. 1986).

Referring to applicant's argument that Morinaga doesn't describe the solution for stripping resists or other organic layers is acknowledged. However, he describes the cleaning/etching solution of the semiconductor, metal, and glass surface (col. 13, line 55-63) and he also teaches that the solution can highly clean the substrate surface without being contaminated with organic materials (col. 28, line 35-36). Since resist films is essentially

Page 6

Application/Control Number: 09/766,896

Art Unit: 1765

organic materials, it would have been obvious that this solution can be used to clean/etch or claimed strip resist films off substrate surface.

#### Conclusion

7. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DuyVu n Deo whose telephone number is 703-305-0515.

DVD February 13, 2003 BENJAMIN L. UTECH
SUPERVISORY PATENT EXAMINER
TECHNOLOGY OFFITTE (700)